

UTC Electronics Department Learning Cycles 2019 – Year 11

Cycle 2.6	Cycle 2.7	Cycle 2.8	Cycle 2.9	Cycle 2.10
<p>Topics</p> <ul style="list-style-type: none"> • ELECTRONIC SYSTEMS AND SUB-SYSTEMS • CIRCUIT CONCEPTS • RESISTIVE COMPONENTS IN CIRCUITS 	<p>Topics</p> <ul style="list-style-type: none"> • SWITCHING CIRCUITS • APPLICATIONS OF DIODES • COMBINATIONAL LOGIC SYSTEMS 	<p>Topics</p> <ul style="list-style-type: none"> • OPERATIONAL AMPLIFIERS • TIMING CIRCUITS 	<p>Topics</p> <ul style="list-style-type: none"> • SEQUENTIAL SYSTEMS • INTERFACING DIGITAL TO ANALOGUE CIRCUITS 	<p>Topics</p> <ul style="list-style-type: none"> • CONTROL CIRCUITS • Extended system design and realisation task Non-exam assessment (NEA) 20% of qualification 40 marks
<p>Keywords</p>	<p>Keywords</p>	<p>Keywords</p>	<p>Keywords</p>	<p>Keywords</p>
<p>Topic Assessments Used</p>	<p>Topic Assessments Used</p>	<p>Topic Assessments Used</p>	<p>Topic Assessments Used</p>	<p>Topic Assessments Used</p>